| Ref<br># | Hits | Search Query            | DBs                                      | Default<br>Operator | Plurals | Time Stamp       |
|----------|------|-------------------------|--|---------------------|---------|------------------|
| L1       | 1903 | processor pipeline swap | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND                 | OFF     | 2006/03/14 17:12 |
| L2       | 1    | "6900668".PN.           | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND                 | OFF     | 2006/03/14 17:12 |
| L3       | 1708 | L1 register             | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND                 | OFF     | 2006/03/14 17:12 |
| L4 .     | 470  | L3 restore              | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND                 | OFF     | 2006/03/14 17:12 |
| L5       | 412  | L4 save                 | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND                 | OFF     | 2006/03/14 17:12 |
| L6       | 332  | L5 delay\$4             | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND                 | OFF     | 2006/03/14 17:12 |
| L7       | 283  | L6 (clock adj cycle\$2) | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND                 | OFF     | 2006/03/14 17:12 |
| L8       | 53   | L7 thread\$2            | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND                 | OFF     | 2006/03/14 17:12 |
| L9       | 1    | L8 swap.AB.             | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND                 | OFF     | 2006/03/14 17:12 |
| L10      | 584  | swap.AB.                | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND                 | OFF     | 2006/03/14 17:12 |
| L11      | 41   | L10 processor pipeline  | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND                 | OFF     | 2006/03/14 17:12 |

|     |     |   | -  |     |     | •                |
|-----|-----|---|--|-----|-----|------------------|
| L12 | 6   | L11 hazard\$2                               | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF | 2006/03/14 17:12 |
| L13 | 6   | L11 (sam\$2 adj register)                   | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF | 2006/03/14 17:12 |
| L14 | 283 | L6 (clock adj cycle\$2)                     | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF | 2006/03/14 17:12 |
| L15 | 4   | L6 swap.AB.                                 | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF | 2006/03/14 17:12 |
| L16 | 137 | (first adj swap)                            | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF | 2006/03/14 17:12 |
| L17 | 8   | L16 processor pipeline                      | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF | 2006/03/14 17:12 |
| L18 | 25  | (register adj swap) pipeline                | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF | 2006/03/14 17:12 |
| L19 | 5   | (register adj swaps) pipeline               | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF | 2006/03/14 17:12 |
| L20 | 4   | L6 (swap adj instruction)                   | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF | 2006/03/14 17:12 |
| L21 | 78  | (register adj swap\$2)                      | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF | 2006/03/14 17:12 |
| L22 | 4   | (register adj swap\$2).AB.                  | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF | 2006/03/14 17:12 |
| L23 | 16  | L21 ((out-or-order) or (out near\$2 order)) | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF | 2006/03/14 17:12 |

| L24 | 17   | L21 ((out-of-order) or (out near\$2 order)) | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF      | 2006/03/14 17:12 |
|-----|------|---|--|-----|----------|------------------|
| L25 | 1903 | processor pipeline swap                     | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF      | 2006/03/14 17:12 |
| L26 | 1708 | L25 register                                | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF      | 2006/03/14 17:12 |
| L27 | 470  | L26 restore                                 | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF      | 2006/03/14 17:12 |
| L28 | 412  | L27 save                                    | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF      | 2006/03/14 17:12 |
| L29 | 332  | L28 delay\$4                                | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF      | 2006/03/14 17:12 |
| L30 | 283  | L29 (clock adj cycle\$2)                    | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF      | 2006/03/14 17:12 |
| L31 | 584  | swap.AB.                                    | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF      | 2006/03/14 17:12 |
| L32 | 137  | (first adj swap)                            | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF<br>· | 2006/03/14 17:12 |
| L33 | 1    | "6900668".PN.                               | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF      | 2006/03/14 17:12 |
| L34 | 53   | L30 thread\$2                               | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF      | 2006/03/14 17:12 |
| L35 | 1    | L34 swap.AB.                                | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF      | 2006/03/14 17:12 |

| L36 | 53 | L30 thread\$2                               | US-PGPUB;                                | AND   | OFF | 2006/03/14 17:12 |
|-----|----|---|--|-------|-----|------------------|
|     |    |   | USPAT;<br>USOCR;<br>IBM_TDB              |       |     |                  |
| L37 | 41 | L31 processor pipeline                      | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND   | OFF | 2006/03/14 17:12 |
| L38 | 6  | L37 hazard\$2                               | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND . | OFF | 2006/03/14 17:12 |
| L39 | 6  | L37 (sam\$2 adj register)                   | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND   | OFF | 2006/03/14 17:12 |
| L40 | 4  | L29 swap.AB.                                | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND   | OFF | 2006/03/14 17:12 |
| L41 | 8  | L32 processor pipeline                      | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND   | OFF | 2006/03/14 17:12 |
| L42 | 25 | (register adj swap) pipeline                | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND   | OFF | 2006/03/14 17:12 |
| L43 | 5  | (register adj swaps) pipeline               | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND   | OFF | 2006/03/14 17:12 |
| L44 | 4  | L29 (swap adj instruction)                  | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND   | OFF | 2006/03/14 17:12 |
| L45 | 78 | (register adj swap\$2)                      | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND   | OFF | 2006/03/14 17:12 |
| L46 | 4  | (register adj swap\$2).AB.                  | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND   | OFF | 2006/03/14 17:12 |
| L47 | 16 | L45 ((out-or-order) or (out near\$2 order)) | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND   | OFF | 2006/03/14 17:12 |

| L48 | 17   | L45 ((out-of-order) or (out near\$2 order))  | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND      | OFF | 2006/03/14 17:12 |
|-----|------|--|--|----------|-----|------------------|
| L49 | 283  | L29 (clock adj cycle\$2)   | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND      | OFF | 2006/03/14 17:12 |
| L50 | 1209 | pipeline\$2 ((out-of-order) or (out<br>near\$2 order)) swap                                    | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND<br>- | OFF | 2006/03/14 17:12 |
| L51 | 4    | pipeline\$2 ((out-of-order) or (out<br>near\$2 order)) (register adj swap)                     | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND      | OFF | 2006/03/14 17:12 |
| L52 | 0    | pipeline\$2 ((out-of-order) or (out<br>near\$2 order)) (swap near5 hazard)                     | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND      | OFF | 2006/03/14 17:12 |
| L53 | 0    | pipeline\$2 ((out-of-order) or (out<br>near\$2 order)) (swap near5 conflict)                   | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND      | OFF | 2006/03/14 17:12 |
| L54 | 109  | pipeline\$2 ((out-of-order) or (out<br>near\$2 order)) swap hazard\$2                          | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND      | OFF | 2006/03/14 17:12 |
| L55 | 95   | pipeline\$2 ((out-of-order) or (out<br>near\$2 order)) (swap near\$3<br>instruction) hazard\$2 | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND      | OFF | 2006/03/14 17:12 |
| L56 | 4    | pipeline\$2 ((out-of-order) or (out<br>near2 order)) (swap near3<br>instruction) hazard\$2     | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND      | OFF | 2006/03/14 17:12 |
| L57 | 2    | pipeline\$2 ((out-of-order) or (out<br>near2 order)) (swap near3<br>instruction) hazard\$2 WAW | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND      | OFF | 2006/03/14 17:12 |
| L58 | 44   | pipeline\$2 ((out-of-order) or (out<br>near2 order)) swap hazard\$2 RAW                        | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND .    | OFF | 2006/03/14 17:12 |
| L59 | 297  | pipeline\$2 ((out-of-order) or (out<br>near2 order)) swap RAW                                  | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND      | OFF | 2006/03/14 17:12 |

| L60 | 0   | pipeline\$2 ((out-of-order) or (out<br>near2 order)) (swap.ab.) RAW | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF | 2006/03/14 17:12 |
|-----|-----|---|--|-----|-----|------------------|
| L61 | 0   | pipeline\$2 ((out-of-order) or (out<br>near2 order)) (swap.AB.) RAW | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF | 2006/03/14 17:12 |
| L62 | 297 | pipeline\$2 ((out-of-order) or (out<br>near2 order)) swap RAW       | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF | 2006/03/14 17:12 |
| L63 | 9   | (swap near5 save) (swap near5 restore)                              | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF | 2006/03/14 17:12 |
| L64 | 1   | (swap near5 save) (swap near5 restore) RAW                          | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF | 2006/03/14 17:12 |
| L65 | . 1 | (swap near5 save) (swap near5 restore) (RAW or WAR)                 | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF | 2006/03/14 17:12 |
| L66 | 274 | (swap adj instruction)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF | 2006/03/14 17:12 |
| L67 | 30  | (swap adj instruction) save restore                                 | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF | 2006/03/14 17:12 |
| L68 | 0   | (swap adj instruction) save restore<br>(WAR OR RAW)                 | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF | 2006/03/14 17:12 |
| L69 | 0   | (swap adj instruction) save restore hazard                          | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF | 2006/03/14 17:12 |
| L70 | 0   | (swap adj instruction) (hazard adj<br>prevention)                   | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF | 2006/03/14 17:12 |
| L71 | 12  | (swap adj instruction) (RAW or - WAR)                               | US-PGPUB;<br>USPAT;<br>USOCR;<br>IBM_TDB | AND | OFF | 2006/03/14 17:12 |